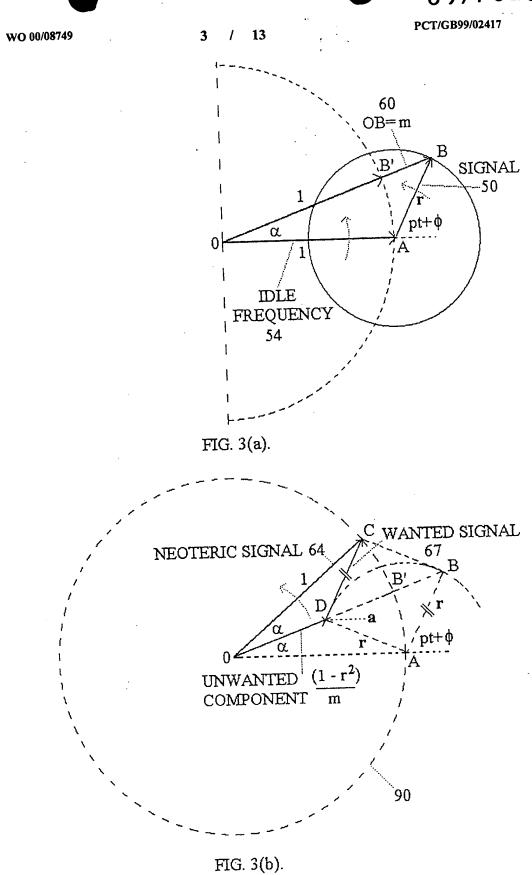
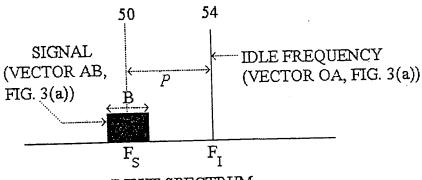


PCT/GB99/02417 WO 00/08749 2 / 13 GENERATE IDLE 56 FREQUENCY Fi  $F_i$ CONTROL IDLE ORIGINAL FREQUENCY LEVEL 58 SIGNAL (OVERLOAD) INPUT 64 \_\_\_50 -54 66 { ADD SIGNAL & IDLE FREQUENCY CONTROL SPECTRUM 52 (TO OBTAIN  $\alpha$ ) 72 AS REQUIRED ---60 \_\_71 APPLY TO INTERNAL PATH (OPTIONAL) 62 APPLY TO 74 AMPLIFIER 66 **〈** --76 FORM NEOTERIC EXTRACT SIGNAL & SIGNAL WITH 65 REJECT UNWANTED MODULATION 2α 68 COMPONENTS ADD BENEFICIAL 67 WANTED SIGNAL DISTORTION 66 (TX EXTRACT SIGNAL & REJECT UNWANTED 68 CONSTRAIN (LIMIT) 66 (X) COMPONENTS ENVELOPE AT ANY OF THESE POINTS 70 Y

FIG. 2.

67 WANTED SIGNAL





INPUT SPECTRUM (SIGNAL MODULATED)

FIG.4(a).

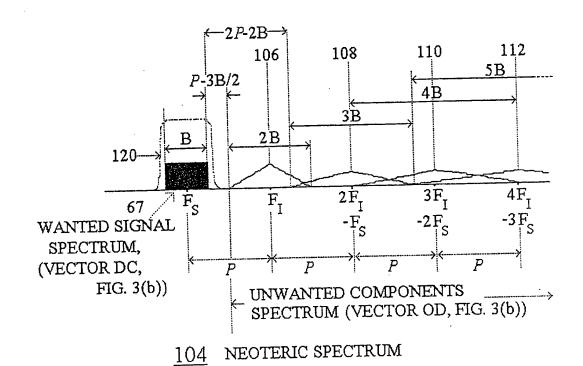
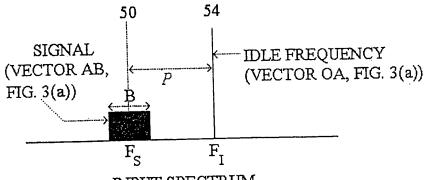


FIG. 4(b).



INPUT SPECTRUM (SIGNAL MODULATED)

FIG.4(a).

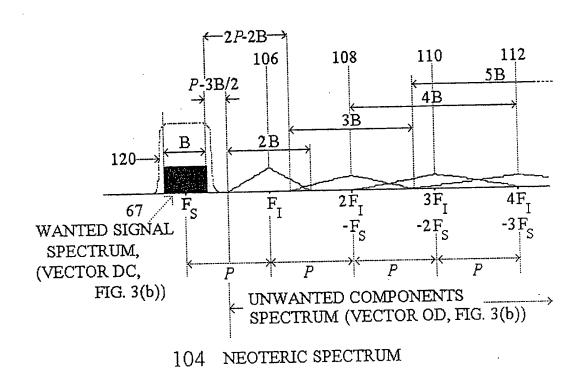
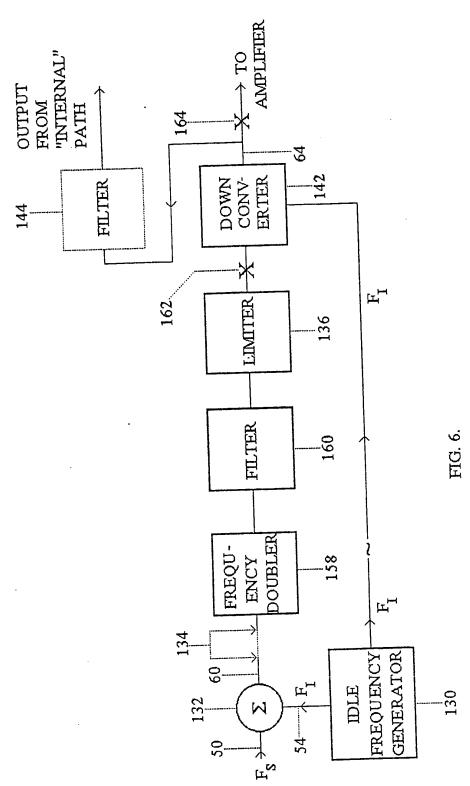


FIG. 4(b).



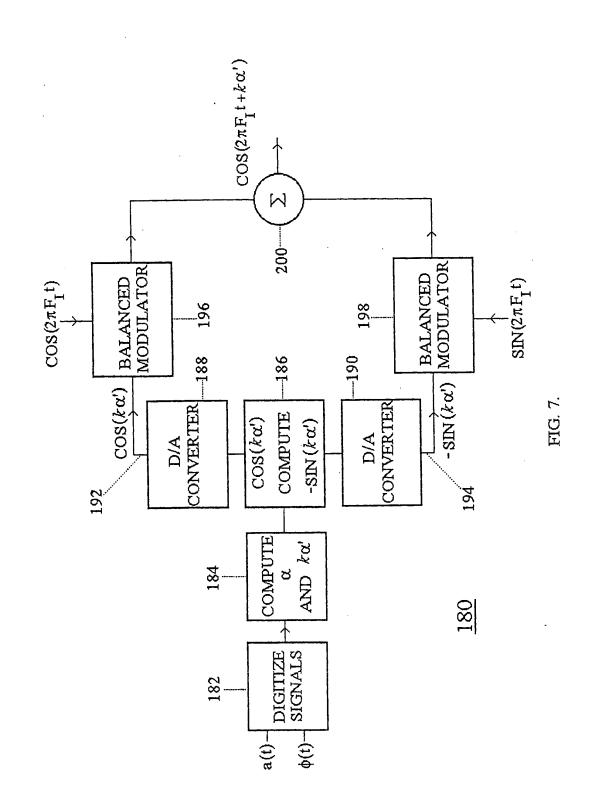
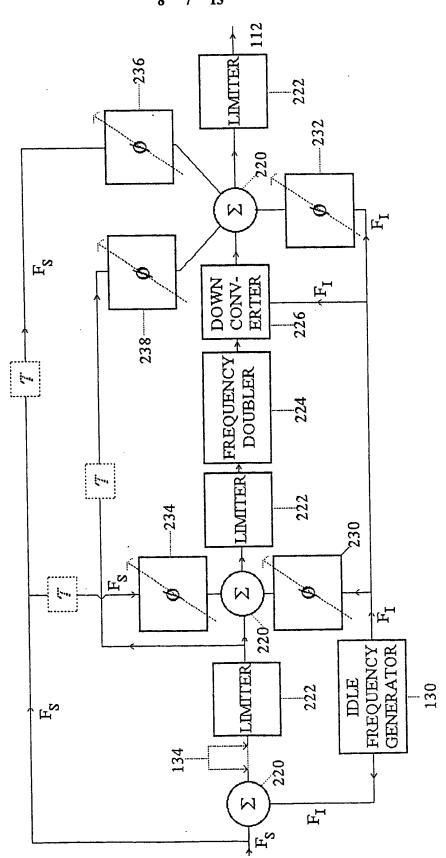


FIG. 8.



DGFSECLF .. OHLSCL

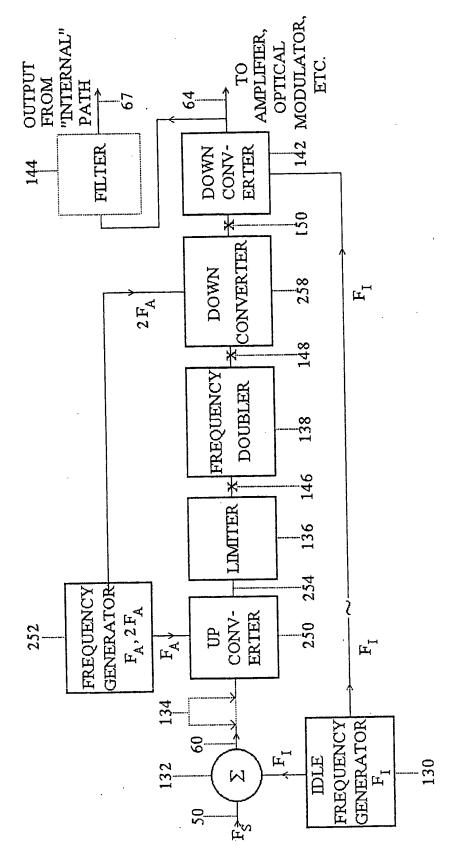


FIG. 9.

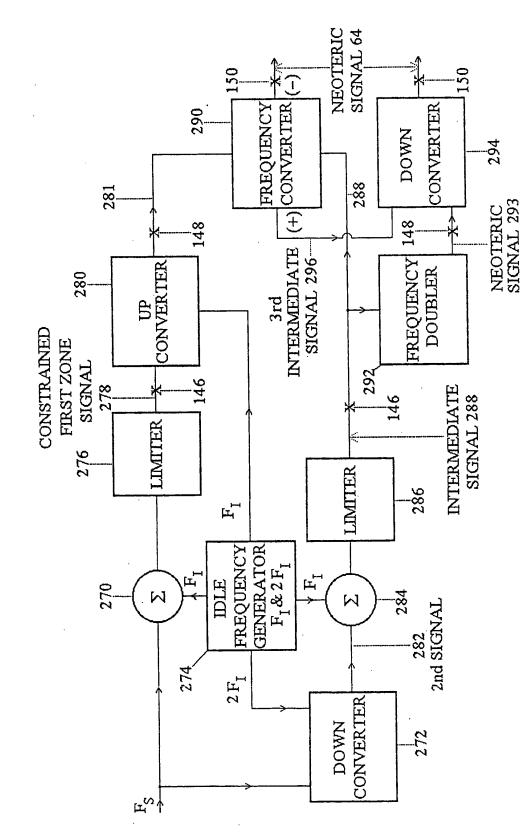
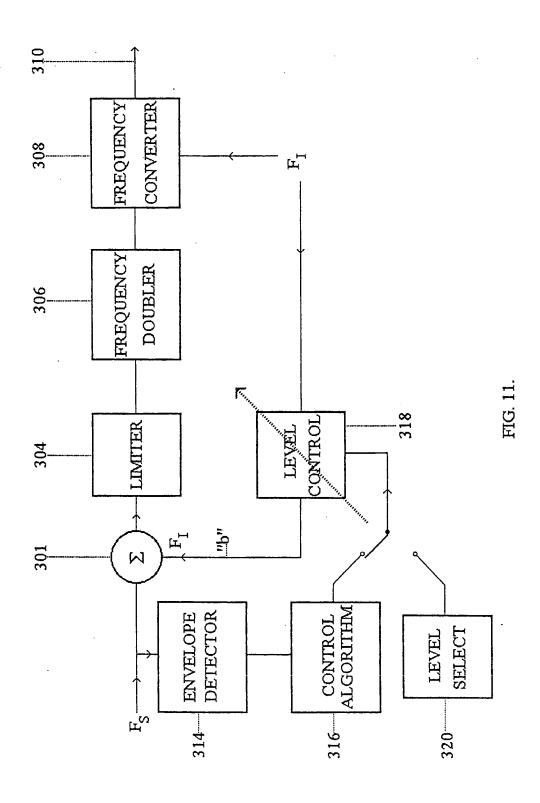


FIG. 10.



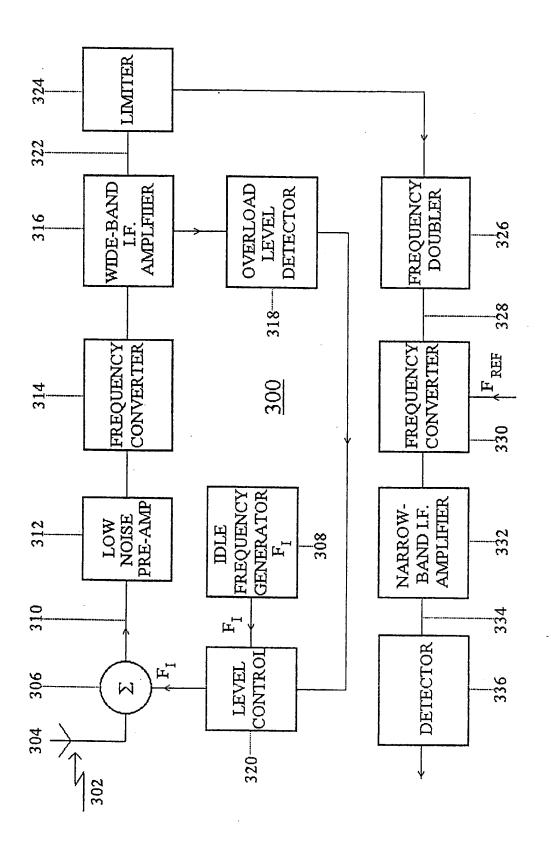


FIG. 12.

PCT/GB99/02417 13 / 13 354 358 356 **SIGNAL AMPLIFIER** BAND PROCESSOR OR SIGNAL **PASS**  $F_S$ 350 **PATH** FILTER 64 360

FIG. 13.

